1			PATENT
2			AUS9-2001-0301-US1
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4	IN THE UNITED STATES PATI	ENT AND TRADEMAR	K OFFICE
5			
6	In re Application of:		
7	Sang Hoo Dhong et al.		
8			
9	Serial No.: 09/915,437	Group Art Unit:	2825
10)		
11	Filed: July 26, 2001		
12)	Examiner:	Binh C. Tat
13	FOR: METHOD OF LOGIC CIRCUIT)		
14	SYNTHESIS AND DESIGN)		
15	USING A DYNAMIC CIRCUIT)	Confirmation No.:	7370
16	LIBRARY)		
17			
18	Mail Stop Amendment		
19	Commissioner for Patents		
20	PO Box 1450		
21	Alexandria, Virginia 22313-1450		
22			
23	RESPONSE TO OFFICE ACTION		
24			
25	This paper is filed in response to the Office Action mailed October 18, 2007, in the		
26	above-identified patent application and is filed within the three-month shortened statutory period		
27	for response set in the Office Action.		
28	Please amend the application as follows:		